Notice of Allowability	Application No.	Applicant(s)
	10/605,135	CHIDAMBARRAO ET AL.
	Examiner	Art Unit
	Asok K. Sarkar	2829
The MAILING DATE of this communication appearable All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate comm IGHTS. This application is	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to <u>amendment filed</u> .		
2. The allowed claim(s) is/are 1-13 and 20.		
3. A The drawings filed on 10 September 2003 are accepted by	the Examiner.	
 4. Acknowledgment is made of a claim for foreign priority urea. a) All b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application	on No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
 CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers hereto or 2) to Paper No./Mail Date including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t 	son's Patent Drawing Review . s Amendment / Comment o .84(c)) should be written on't	r in the Office action of he drawings in the front (not the back) of
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. Interview S Paper No. 8), 7. Examiner's	formal Patent Application (PTO-152) ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance

DETAILED ACTION

Response to Amendment

1. Applicant's explanation of the instant invention in pointing the difference with the cited prior art was found to be persuasive.

Allowable Subject Matter

- 2. Claims 1 13 and 20 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

Claims 1 – 13 and 20 recite, inter alia, a method for manufacturing an integrated circuit comprising a plurality of semiconductor devices including an n – type and a p – type transistor comprising the step of heating the circuit such that the deposited low-resistance material reacts with the polysilicon of both transistors such that compressive stress develops along the longitudinal direction of the p – type transistor. The art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

Conclusion

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Application/Control Number: 10/605,135

Art Unit: 2829

5. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure. En, US 6,573;172 teaches a method of producing a CMOS

device by forming stressed films in PMOS and NMOS transistors.

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Asok K. Sarkar whose telephone number is 571 272

1970. The examiner can normally be reached on Monday - Friday (8 AM- 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nestor Ramirez can be reached on 571 272 2034. The fax phone number

for the organization where this application or proceeding is assigned is 703-872-9306.

7. Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Asok K. Sarkar

December 30, 2004

Patent Examiner

EVAN PERT
PRIMARY EXAMINER

Page 3